REVIEW



Comments on "Chaotic oscillator based on memcapacitor and meminductor" (Nonlinear Dyn, DOI: 10.1007/s11071-019-04781-5)

Ivo Petráš

Received: 14 June 2020 / Accepted: 9 October 2020 / Published online: 24 October 2020 © Springer Nature B.V. 2020

Abstract In this brief comment, an enhancement of results published in the article "Chaotic oscillator based on memcapacitor and meminductor" (Nonlinear Dyn, DOI: https://doi.org/10.1007/s11071-019-04781-5), is described. It was shown that the proposed chaotic oscillator can be extended to its fractional-order form, where various dynamical properties, as for example, the one-scroll attractor can be observed. A simple numerical solution of the new fractional-order chaotic system for simulations, further analysis and investigation is presented as well.

Keywords Memristive system · Fractional calculus · Fractional-order chaotic system · Chaotic oscillator

1 Introduction

Research in area of chaotic systems and their analysis has quite a long tradition. Recently a new type of electrical circuits which exhibit chaos have been investigated. Such oscillators are represented by simple electrical circuits consist of new electrical elements, such as memristor, memcapacitor, and meminductor. It opens a new area of chaotic systems, for example, see [1–4].

On the other hand, the subject of fractional calculus has reached popularity and importance during the past

I. Petráš (🖂)

few decades. The basic symptoms, which should be presented in the system in order to the fractional calculus should be used are: recursion, fractality, roughness, heredity, (long) memory. Fractional calculus, a.k.a. non-integer calculus, is known since the classical calculus with the first written reference dated September 1695 in the letters between Leibniz and L'Hospital. Nowadays, the fractional calculus has a wide area of applications in various fields as well as in the chaotic systems [5].

Taking into account above considerations, we may combine the fractional calculus technique and modeling of the chaotic oscillators, where the new concept of electrical circuits with memristive elements is used [6-8].

This brief comment is organized as follows. Section 1 introduces the problem and main motivation. In Sect. 2 the fractional calculus fundamentals are described. In Sect. 3 the memristive systems are discussed. Section 4 presents a new model of the fractional-order chaotic system. In Sect. 5 the simulation results are presented. In Sect. 6 the conclusions of this comment and further possible research in this area are discussed.

2 Preliminaries

2.1 Definition of fractional-order derivative/integral

Fractional calculus is a generalization of integration and differentiation to joint non-integer γ -order operator

Faculty of BERG, Technical University of Košice, Němcovej 3, 042 00 Košice, Slovak Republic e-mail: ivo.petras@tuke.sk

 ${}_{a}D_{b}^{\gamma}$, where *a* and *b* are the bounds of the operation. The standard notation for denoting the common left-sided integro-differential operator of a function f(t) defined within the interval [a, t] is ${}_{a}D_{t}^{\gamma}f(t)$, with $\gamma \in \mathbb{R}$.

There exist many definitions for the fractional-order operator (fractional-order integrals for $\gamma < 0$ and derivatives for $\gamma > 0$) but in this article, we will restrict only on the Caputo's definition (CD) and the Grünwald-Letnikov definition (GLD), respectively.

The CD, for $n - 1 < \gamma < n$, can be written as [9]:

$${}_{a}D_{t}^{\gamma}f(t) = \frac{1}{\Gamma(n-\gamma)} \int_{a}^{t} \frac{f^{(n)}(\tau)}{(t-\tau)^{\gamma-n+1}} d\tau.$$
(1)

In case of electrical circuits, where the fractional derivative is used in fractional-order model of circuit elements, the Caputo's definition can be used because the initial conditions for the fractional differential equations with the Caputo derivatives are in the same form as for ordinary differential equations, i.e., $f^{(n)}(0) = c_n$, $\forall n \in \mathbb{N}$.

The GLD is given as follows [9, 10]:

$${}_{a}D_{t}^{\gamma}f(t) = \lim_{h_{s}\to 0}\frac{1}{h_{s}^{\gamma}}\sum_{i=0}^{\left\lfloor\frac{t-a}{h_{s}}\right\rfloor}(-1)^{i}\binom{\gamma}{i}f(t-ih_{s}), \quad (2)$$

where $\lfloor z \rfloor$ is the floor function, i.e., the greatest integer smaller than *z*, and

$$\binom{\gamma}{i} = \frac{\Gamma(\gamma+1)}{\Gamma(i+1)\,\Gamma(\gamma-i+1)} \tag{3}$$

are the binomial coefficients for $\binom{\gamma}{0} = 1$. This form of the derivative definition is very helpful for obtaining a numerical solution of fractional differential equation.

2.2 Numerical solution of fractional differential equation

Since the both definitions CD and GLD are equivalent for a wide class of the functions, for numerical calculation of fractional-order derivative we can use the relation (4) derived from the GLD (2). The relation for the numerical approximation of γ -th derivative at the points kh_s , (k = 1, 2, 3, ...) has the following form [9]:

$$_{(k-L_m/h_s)} D_{t_k}^{\gamma} f(t) \approx h_s^{-\gamma} \sum_{i=0}^k c_i^{(\gamma)} f(t_{k-i}),$$
 (4)

where L_m is the "memory length", $t_k = kh_s$, h_s is the time step of calculation (definition (4) is valid only as h_s

tends toward 0 and that the accuracy of the simulation depends on the value of h_s) and $c_i^{(\gamma)}$ (i = 0, 1, 2, ...) are the binomial coefficients. For their calculation we may use the following expression:

$$c_0^{(\gamma)} = 1, \qquad c_i^{(\gamma)} = \left(1 - \frac{1+\gamma}{i}\right)c_{i-1}^{(\gamma)}.$$
 (5)

Thus, general numerical solution of the fractional differential equation (initial value problem)

$${}_0D_t^{\gamma}x(t) = f(x(t), t),$$

can be expressed as follows [5,11]:

$$x(t_k) = f(x(t_k), t_k) h_s^{\gamma} - \sum_{i=0}^k c_i^{(\gamma)} x(t_{k-i}).$$
(6)

For the memory term expressed by the sum, a "short memory" principle for various length L_m can be used.

An evaluation of the short memory effect and convergence relation of the error between short and long memory were very clearly described and proved in [9].

3 Fractional-order memristive systems

There are a huge number of electrical circuit where the fractional calculus can be used (e.g., [12–14], etc.); however, the classical circuit theory is limited to the variables v, i, q, and ϕ , which are used for description of all four basic devices (resistor, capacitor, inductor, and memristor). The memristor was postulated by Leon Chua in 1971 [15] and manufactured by HP in 2008 [16]. Moreover, other devices (memcapacitor, meninductor) recently introduced in [17] are located beyond these limits. Certain circuit elements, memcapacitor and meninductor, involve not only those four variables but also the time-integrals of the charge and the flux [18]. These new state variables lead to the so-called "mem" systems, which are a class of higher-order devices. These memory elements are a general term and belong to group of memristive systems [19]. Such memristive systems can be described by fractionalorder models [6,7,20-22]. This notion is also based on the fact, that there is not ideal electrical element and the real elements lie in between two ideal, e.g., fractor or fractductor [5,13,23].

Since, the real capacitor and the real inductor have been investigated by many authors and the experimental evidences of their real orders of the models were confirmed [14,24,25], real memristor, memcapacitor, and meminductor, were not investigated experimentally yet, just by simulation models or by the emulators [26–28].

However, there are many electrical circuits, mainly oscillators, where memristor, memcapacitors, and meminductors, have been used [29,30]. Nowadays, application of such elements also in other type of circuits rapidly grows, for instance, new kind of PID controller [31], neuron network model [32], and so on.

Le us recall to a linear capacitor model proposed in 1994 by Westerlund and Ekstam [24]. For a general input voltage v(t), applied at t = 0, the current is $i(t) = C_0 D_t^{\alpha} v(t)$, $0 < \alpha \le 1$, for t > 0, (7) where *C* is the capacitance of the capacitor with unit [F/s^{1- α}]. It is related to the kind of dielectric. Another constant α (order), $\alpha \in \mathbb{R}$, is related to the losses of the capacitor. Westerlund and Ekstam provided in their work the table of various capacitor dielectrics with appropriate constant α which has been obtained experimentally by measurements on the real capacitors.

By using the relations, described in [18], for memcapacitor and meminductor and well-known relations between four basic elements (resistor, capacitor, inductor, memristor), we can obtain another floor of the four square symmetry, which is depicted in Fig. 1.

As we can observe in Fig. 1, some additional electrical elements, were not discovered so far.

4 Model of the fractional-order chaotic system

In paper [30], the chaotic oscillator circuit based on the resistor, capacitor, negative resistor, memcapacitor and meminductor was introduced. According to Kirchhoff laws and additional scale transformation the chaotic system can be presented as the following dimensionless dynamical system:

$$\frac{dx(t)}{dt} = az(t) + bz(t)w(t) - x(t),
\frac{dy(t)}{dt} = dy(t) + ey(t)v^{2}(t) - fz(t) - gz(t)w(t),
\frac{dz(t)}{dt} = hy(t) + jy(t)v^{2}(t) - kx(t),
\frac{dw(t)}{dt} = n_{1}z(t),
\frac{dv(t)}{dt} = n_{2}y(t),$$
(8)

where for the parameters: a = 1.73, b = -2.04, d = 0.46, e = 0.04, f = 0.67, g = 0.19, h = 0.48, j = 0.48, j



Fig. 1 Connection of known electrical elements (Inspired by Refs. [8,18])

0.52, $k = n_1 = n_2 = 0.21$, the initial values setting: x(0) = 0.2, y(0) = 0.5, z(0) = 0.45, w(0) = 0.1, v(0) = 0.5, the chaotic attractor was obtained.

Taking into account the consideration described in previous section, the fractional-order models of the elements used in the chaotic oscillator (Fig. 5 in [30]) could be applied to Kirchhoff laws. Then, instead of the system (8), we obtain the following set of fractional differential equations:

$${}_{0}D_{t}^{\gamma_{1}}x(t) = az(t) + bz(t)w(t) - x(t),$$

$${}_{0}D_{t}^{\gamma_{2}}y(t) = dy(t) + ey(t)v^{2}(t) - fz(t) - gz(t)w(t),$$

$${}_{0}D_{t}^{\gamma_{3}}z(t) = hy(t) + jy(t)v^{2}(t) - kx(t),$$

$${}_{0}D_{t}^{\gamma_{4}}w(t) = n_{1}z(t),$$

$${}_{0}D_{t}^{\gamma_{5}}v(t) = n_{2}y(t),$$
(9)

Deringer

where γ_1 , γ_2 , γ_3 , γ_4 , and γ_5 are the real orders of the elements used in the chaotic oscillator, described in [30].

For simulation purposes we will use a numerical solution of the fractional deferential equations (9) obtained by using the relationships (4) and (5) as well as the principle (6), which lead to equations in the form:

$$\begin{aligned} x(t_k) &= [az(t_{k-1}) + bz(t_{k-1})w(t_{k-1}) - x(t_{k-1})]h_s^{\gamma_1} \\ &- \sum_{i=0}^k c_i^{(\gamma_1)} x(t_{k-i}), \\ y(t_k) &= [dy(t_{k-1}) + ey(t_{k-1})v(t_{k-1})^2 - fz(t_{k-1}) \\ &- gz(t_{k-1})w(t_{k-1})]h_s^{\gamma_2} - \sum_{i=0}^k c_i^{(\gamma_2)} y(t_{k-i}), \\ z(t_k) &= [hy(t_{k-1}) + jy(t_{k-1})v(t_{k-1})^2 - kx(t_{k-1})]h_s^{\gamma_2} \\ &- \sum_{i=0}^k c_i^{(\gamma_3)} z(t_{k-i}), \\ w(t_k) &= [n_1 z(t_{k-1})]h_s^{\gamma_4} - \sum_{i=0}^k c_i^{(\gamma_4)} w(t_{k-i}), \end{aligned}$$

$$v(t_k) = [n_2 y(t_{k-1})] h_s^{\gamma_5} - \sum_{i=0}^k c_i^{(\gamma_5)} v(t_{k-i}), \qquad (10)$$

where T_{sim} is the simulation time, k = 1, 2, 3, ..., N, for $N = [T_{sim}/h_s]$, and (x(0), y(0), z(0), w(0), v(0))is the start point (initial conditions). The binomial coefficients $c_i^{(\gamma_1)}, c_i^{(\gamma_2)}, c_i^{(\gamma_3)}, c_i^{(\gamma_4)}$, and $c_i^{(\gamma_5)}$ are calculated according to relation (5), respectively.

For further simulations and analysis the numerical solution (10) of the system (9) was coded in MATLAB.

5 Simulation results

Let us use proposed numerical solution (10) to simulate chaotic system (9) for the same parameters and initial conditions setting as they were defined for chaotic system (8), for simulation time $T_{sim} = 1000$ s, time step $h_s = 0.005$, derivative orders $\gamma_1 = 0.9$, $\gamma_2 = 1$, $\gamma_3 = 1$, $\gamma_4 = 1$, and $\gamma_5 = 1$. Let us assume $\gamma_1 = 0.9$, which corresponds to the real order of many capacitors as well as the capacitor C_1 used in the chaotic oscillator circuit with the real capacitor C_1 the order γ_1 should be experimentally measured. However, in [30] was not built the real circuit and therefore is unknown the capacitor model number. Moreover, other elements, namely



Fig. 2 Chaotic attractor in x - y state plane of system (9)



Fig. 3 Chaotic attractor in x - w state plane of system (9)

memcapacitor C_M and meminductor L_M , in circuit are just artificial and were not manufactured yet.

In Figs. 2, 3, 4, 5, 6 and 7 are depicted chaotic behavior (one-scroll attractor) of the system (9) for the parameters the initial conditions setting as in (8), and $\gamma_1 = 0.9$, $\gamma_2 = \gamma_3 = \gamma_4 = \gamma_5 = 1$, in various state planes, respectively.

The Lyapunov exponents of the system (9), computed according to method described in [33], for the parameters as in (8), and orders $\gamma_1 = 0.9$, $\gamma_2 = \gamma_3 =$ $\gamma_4 = \gamma_5 = 1$, have the following values: 0.5807, -0.1683, -0.0047, -0.9647, -1.0267, which confirm the chaotic behavior because at least one exponent is positive.



Fig. 4 Chaotic attractor in y - w state plane of system (9)



Fig. 5 Chaotic attractor in z - w state plane of system (9)



Fig. 6 Chaotic attractor in z - v state plane of system (9)



Fig. 7 Chaotic attractor in w - v state plane of system (9)

6 Conclusions

In this comment, we mentioned that the results presented in paper [30] can be enhanced. Proposed chaotic oscillator consists of meminductor and memcapacitor as well as resistor, capacitor and negative resistor, can be described by fractional differential equations. Applying fractional-order derivative into the mathematical model of the oscillator elements, a memory effect and other dynamical properties are caught. Moreover, we obtain more degree of freedom due to the derivative orders.

Since, the orders of the real capacitors and inductors models have been investigated and experimentally confirmed, there is not an experimental evidence of real orders in model of memristors, meminductors and memcapacitors because they are not commercially produced and sold. However, proposed fractional-order model of the chaotic oscillator allows us to use the fractional orders, except for capacitor C_1 order γ_1 , also for memcapacitor C_M order γ_2 as well as for meminductor L_M order γ_3 . Even more, instead negative resistor G a memristor can be used. This is an idea for further research in this field of chaotic oscillator circuits. Moreover, it opens a new area for research of the chaotic systems dynamics.

Acknowledgements This work was supported in part by the Slovak Grant Agency for Science under Grant VEGA 1/0365/19, and by the Slovak Research and Development Agency under the contracts No. APVV-14-0892 and No. APVV-18-0526 and

COST Action CA15225 a network supported by COST (European Cooperation in Science and Technology).

Compliance with ethical standards

Conflicts of interest The author declares that he has no conflict of interest.

References

- Itoh, M., Chua, L.O.: Memristor oscillation. Int. J. Bifurcat. Chaos Appl. Sci. Eng. 18, 3183–3206 (2008)
- Liao, X., Mu, N.: Self-sustained oscillation in a memristor circuit. Nonlinear Dyn. 96, 1267–1281 (2019)
- Yuan, F., Deng, Y., Li, Y., Wang, G.: The amplitude, frequency and parameter space boosting in a memristormeminductor-based circuit. Nonlinear Dyn. 96, 389–405 (2019)
- Yuan, F., Li, Y., Wang, G., Dou, G., Chen, G.: Complex dynamics in a memcapacitor-based circuit. Entropy 21, 188 (2019)
- Petráš, I.: Fractional-Order Nonlinear Systems. Springer, New York (2011)
- Petráš, I., Chen, Y. Q., Coopmans, C.: Fractional-order memristive systems, In: Proceedings of the 14th IEEE International Conference on Emerging Technologies & Factory Automation, September 22–25, Palma de Mallorca, Spain, pp.1251–1258 (2009)
- Coopmans, C., Petráš, I., Chen, Y. Q.: Analogue Fractional-Order Generalized Memristive Devices, In: Proc. of the ASME 2009 International Design Engineering Technical Conferences and Computers and Information in Engineering Conference, DETC2009/MSNDC-86861, San Diego, USA, August 30–September 2 (2009)
- Petráš, I., Chen, Y.Q.: Fractional-order circuit elements with memory, In: Proc. of the IEEE 13th International Carpathian Control Conference, Podbanske, Slovak Republic, May 28– 31, pp. 552–555 (2012)
- 9. Podlubny, I.: Fractional Differential Equations. Academic Press, San Diego (1999)
- Oldham, K.B., Spanier, J.: The Fractional Calculus. Academic Press, New York (1974)
- Petráš, I.: Comments on "Coexistence of hidden chaotic attractors in a novel no-equilibrium system" (Nonlinear Dyn, doi.org/10.1007/s11071-016-3170-x). Nonlinear Dyn. 90, 749–754 (2017). https://doi.org/10.1007/ s11071-017-3671-2
- Arena, P., Caponetto, R., Fortuna, L., Porto, D.: Nonlinear Noninteger Order Circuits and Systems: An Introduction. World Scientific, Singapore (2000)
- Elwakil, A.S.: Fractional-order circuits and systems: an emerging interdisciplinary research area. IEEE Circuits Syst. Mag. 10, 40–50 (2010)
- Schafer, I., Kruger, K.: Modelling of lossy coils using fractional derivatives. J. Phys. D Appl. Phys. 41, 1–8 (2008)
- Chua, L.O.: Memristor: the missing circuit element. IEEE Trans Circuit Theory CT-18, 507–519 (1971)
- Strukov, D.B., Snider, G.S., Stewart, D.R., Williams, R.S.: The missing memristor found. Nature 453, 80–83 (2008)

- Ventra, M.Di, Pershin, Y.V., Chua, L.O.: Circuit elements with memory: memristors, memcapacitors and meminductors. Proc. IEEE 97, 1717–1724 (2009)
- Biolek, D., Biolek, Z., Biolkova, V.: SPICE modeling of memristive, memcapacitative and meminductive systems, In: Proc. of the European Conference on Circuit Theory and Design, August 23–27, Antalya, pp. 249–252 (2009)
- Chua, L.O., Sung, M.K.: Memristive devices and systems. Proc. IEEE 64, 209–223 (1976)
- Petráš, I.: Fractional-order memristor-based Chua's circuit. IEEE Trans. Circuits Syst. II, Exp. Briefs 57, 975–979 (2010)
- Yang, N., Xu, Ch., Wu, Ch., Jia, R., Liu, Ch.: Fractionalorder cubic nonlinear flux-controlled memristor: theoretical analysis, numerical calculation and circuit simulation. Nonlinear Dyn. 97, 33–44 (2019)
- Zhou, P., Ma, J., Tang, J.: Clarify the physical process for fractional dynamical systems. Nonlinear Dyn. 100, 2353– 2364 (2020)
- Caponetto, R., Dongola, G., Fortuna, L., Petráš, I.: Fractional Order Systems: Modeling and Control Applications. World Scientific, Singapore (2010)
- Westerlund, S., Ekstam, L.: Capacitor theory. IEEE Trans. Dielectr. Electr. Insul. 1, 826–839 (1994)
- Westerlund, S.: Dead Matter Has Memory!. Causal Consulting, Kalmar (2002)
- Lopez, C. S., Gomez, V. H. C., Aguilar, M. A. C., Perez, I. C.: Fractional-order memristor emulator circuits. Complexity (2018), 2806976
- Pershin, Y.V., Ventra, M.Di: Memristive circuits simulate memcapacitors and meminductors. Electron. Lett. 46, 517– 518 (2010)
- Taskiran, Z.G.C., Sagbas, M., Ayten, U. E., Sedef, H.: A new universal mutator circuit for memcapacitor and meminductor elements. Int. J. Electron. Commun. 119, 153180 (2020)
- Ma, X., Mou, J., Liu, J., Ma, Ch., Yang, F., Zhao, X.: A novel simple chaotic circuit based on memristor-memcapacitor. Nonlinear Dyn. 100, 2859–2876 (2020)
- Wang, X., Yu, J., Jin, Ch., Iu, HHCh., Yu, S.: Chaotic oscillator based on memcapacitor and meminductor. Nonlinear Dyn. 96, 161–173 (2019)
- Lopez, C.S., Gomez, V.H.C., Aguilar, M.A.C., Lopez, F.E.M.: PID controller design based on memductor. Int. J. Electron. Commun. 101, 9–14 (2019)
- Bao, H., Zhang, Y., Liu, W., Bao, B.: Memristor synapsecoupled memristive neuron network: synchronization transition and occurrence of chimera. Nonlinear Dyn. 100, 937– 950 (2020)
- Wolf, A., Swift, J.B., Swinney, H.L., Vasano, J.A.: Determining Lyapunov exponents from a time series. Physica D 16, 285–317 (1985)

Publisher's Note Springer Nature remains neutral with regard to jurisdictional claims in published maps and institutional affiliations.